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**June 30, 2003**

**Sau Ching Wong**

2824

**Hur, J.**

MLM003US1D

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Jy H. H.

1/7/05

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Sheet		2	of		2
		Application Number			
		Filing Date		June 30, 2003	
		First Named Inventor		Sau Ching Wong	
		Group Art Unit		2824	
		Examiner Name		Hur, J.	
		Attorney Docket Number		MLM003US1D	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
JA	*6	Intel, "Preliminary Datasheet for 1.8 Volt Intel® Wireless Flash Memory (W18)," Order No. 290701-002 (January 2001) Pages i-79.	
JA	*7	Pathak, B. et al., 2001 IEEE International Solid-State Circuits Conference, Session 2, Non-volatile Memories, "2.3 A 1.8V 64Mb 100MHz Flexible Read-While Write Flash Memory" (Feb. 5, 2001).	

Examiner Signature		Date Considered	1/7/05
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